AMENDMENTS TO THE CLAIMS

1. – 11. (Canceled)

12. (Original) A receiving device for receiving data multiplex signal, comprising:

an extracting means for extracting a music piece broadcast end bit and an M/S flag from a multiplex signal, said music piece broadcast end bit being reversed in its logic being synchronized with an end of a music piece broadcasted, and said M/S flag indicating music or speech; and

an outputting means for outputting a signal indicating a division between music pieces, in synchronism with a point of time at which said music piece broadcast end bit is reversed in its logic in a state that said M/S flag indicates music.

13. (Original) A receiving device for receiving multiplex signal, comprising:

an extracting means for extracting an information bit indicating a number of remaining music pieces to be broadcasted and an M/S flag indicating music or speech;

a signal generating means for generating a signal that indicates a value indicated by said information bit, when said M/S flag indicates music.

14. (Original) The receiving device according to Claim 12, wherein:

said signal generating means outputs a signal indicating an end of broadcasting a music piece, in synchronism with change of a value indicated by said information bit.

15. (Original) A system controller for controlling a receiving device and recording device which are interconnected, wherein:

said receiving device extracts data to be transmitted and a code indicating an ordinal number from a received signal, said code corresponding to each content element out of a series of content elements expressed by said data, and said receiving device outputs said extracted data and code; and

said system controller comprises:

an interface means for receiving an operation of designating an ordinal number corresponding to some one out of the series of content elements; and

a control means for causing the recording device to start to record, when said code indicating the designated ordinal number is inputted.

16. (Original) A recording device for receiving data multiplex signal and recording contents of said data multiplex signal, comprising:

a recording means for recording data;

an interface means for receiving an instruction to start recording;

an extracting means for extracting data expressing contents and a divisional signal concerning contents of said data, from the data multiplex signal received;

a buffer memory means for storing said extracted data and said divisional signal over a certain period;

a detecting means for detecting a point of time at which a state of the divisional signal stored in said buffer memory means changes; and

a transfer means for supplying data stored in said buffer memory means since said detected point of time at which the state of the divisional signal changed, to said recording means, when the instruction is received by said interface means.

17. (Original) The recording device according to Claim 16, further comprising:

a display means for displaying a time period obtained by subtracting a period elapsed from said detected point of time at which the state of the divisional signal changed from said certain period for which said buffer memory means can store the data.